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FORM PTO-1449

THIRD SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
0056.10USAPPLICATION NO.
09/223,046APPLICANT
van Hook et al.FILING DATE
December 30, 1998GROUP
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA4	5,150,290	09/1992	Hunt	364	736	
	AB4	5,418,915	05/1995	Matuda et al.	395	325	
	AC4	5,581,773	12/1996	Glover	395	800	
	AD4	5,669,010	09/1997	Duluk, Jr.	395	800.22	
	AE4	5,752,071	05/1998	Tubbs et al.	395	800.34	
	AF4	5,758,176	05/1998	Agarwal et al.	395	800	
	AG4	5,778,241	07/1998	Bindloss et al.	395	800.2	
	AH4	5,784,602	07/1998	Glass et al.	395	561	
	AI4	5,812,723	09/1998	Ohtsu et al.	385	128	
	AJ4	5,822,606	10/1998	Morton	395	800.16	
	AK4	5,838,984	11/1998	Nguyen et al.	395	800.05	

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	AP	12	MIPS Technologies' Proposed Claim Construction Statement Pursuant to Local Rule 16-10(A) with Exhibits A and B, 15 Pages, Filed April 17, 2000 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.				
	AQ	12	An Introduction to Thumb™, Version 2.0, Advanced RISC Machines, 29 pages (March 1995).				
	AR	12	Gwennap, L., "Digital, MIPS Add Multimedia Extensions," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 15, pp. 1-5 (November 18, 1996).				
	AS	12	Gwennap, L., "New Multimedia Chips to Enter the Fray," <i>Microprocessor Report</i> , MicroDesign Resources, Vol. 10, No. 13, 1 page (October 1996).				

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LARRY D. DONAGHUE
PRIMARY EXAMINER

DATE CONSIDERED

09/04/02

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	AA5	5,864,703	01/1999	van Hook et al.	395	800.22	
	AB5	6,088,783	07/2000	Morton	712	22	
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	AP	<u>13</u>	QML-DSP/MCM and Die Courier, at http://www.ti.com/sc/docs/military/liter/ecour/dsp.htm , 7 pages (January 1996).
	AQ	<u>13</u>	A236 Parallel Digital Signal Processor Chip Programmer's Reference Manual, Oxford Micro Devices, Inc., 195 pages (1994). (Bates Numbers L11184-L11378).
	AR	<u>13</u>	Piccolo - The ARM Signal Processing Architecture, at http://www.calab.cs.pusan.ac.kr/TechnicalReport... , 12 pages (last visited March 6, 2001).
	AS	<u>13</u>	Microprocessor Devices Reliability Monitor Report: Third Quarter 1997, Motorola, 8 pages (1997).

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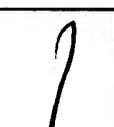


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	AP	<u>14</u>	Reliability Monitor Environmental Stress Data 3Q97, Motorola, 4 pages (1997).
	AQ	<u>14</u>	Reliability and Quality Report 3Q97, Motorola, 2 pages (1997).
	AR	<u>14</u>	Microprocessor Devices Reliability Monitor Report: Fourth Quarter 1997, Motorola, 10 pages (1997).
	AS	<u>14</u>	Reliability and Quality Report 4Q97, Motorola, 2 pages (1997).

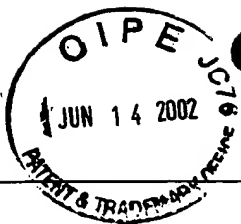
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	AP	15	"Hardware Implications of xmem as a st followed by a ld," Motorola Semiconductor Engineering Bulletin, Motorola, 5 pages (September 1992).
	AQ	15	601 First Silicon, at http://www.mot.com/SPS/PowerPC/library/press_releases/601_First_Silicon.html , 2 Pages (October 1, 1992).
	AR	15	"Running the MC88110 in Lockstep," Motorola Semiconductor Engineering Bulletin, Motorola, 2 pages (1992).
	AS	15	MC88410UM/AD: MC88410 Secondary Cache Controller User's Manual, at http://www.mot-sps.com/lit/html/mc88410umad.html , 1 page (January 24, 1997).

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	AP	16	AN1214: MC88110 64-bit External Bus Interface to 16-bit EPROM, at http://www.mot-sps.com/lit/html/an1214.html , 1 page (January 24, 1997).
	AQ	16	TMS320C80 Digital Signal Processor, Texas Instruments, p. 41 (July 1994).
	AR	16	EB162: Programming Tips (MC88110), at http://www.mot-sps.com/lit/html/eb162.html , 1 page (January 24, 1997).
	AS	16	MC88110/410DH/AD: MC88110/MC88410 Designer's Handbook, at http://www.mot-sps.com/lit/html/mc88110410dhad.html , 1 page (January 24, 1997).

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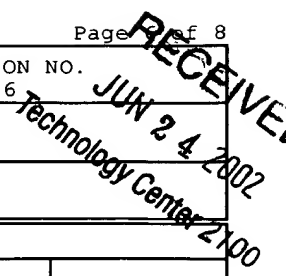
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	AP	17	MC88110UMAD: MC88110 Second Generation RISC Microprocessor User's Manual, at http://www.mot-sps.com/lit/html/mc88110umad.html , 1 page (January 24, 1997).
	AQ	17	Weiss, R., ARM Piccolo Mixes ARM RISC with DSP, at http://www.estd.com/Editorial/1996/11/Briefs/arm.html , 1 page, (November 1996).
	AR	17	"Interrupt Latency in the MC88110," Motorola Semiconductor Engineering Bulletin, Motorola, pp. 1, 2 and 4-9 (1992).
	AS	17	MIPS Technologies' Opposition To Lexra, Inc.'s Motion For Summary Judgement Of Invalidity Or Noninfringement of U.S. Patent No. 5,864,703 And Of Invalidity of U.S. Patent No. 4,814,976, 26 Pages, Filed February 20, 2001 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.

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	AP	18	Declaration of David A. Nelson In Support Of MIPS Technologies' Brief In Opposition To Lexra, Inc.'s Motion For Summary Judgement Of Invalidity Or Noninfringement of U.S. Patent No. 5,864,703 And Of Invalidity of U.S. Patent No. 4,814,976, 4 Pages, Filed February 20, 2001 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AQ	18	Declaration Of Steven L. Scott, 4 Pages, Dated September 20, 2000, Filed in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AR	18	Plaintiff MIPS Technologies' Opening Brief In Support Of Its Position Regarding Claim Construction, 21 Pages, Filed August 17, 2000 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AS	18	Condensed Transcript of Claims Construction Hearing, 52 Pages, Dated April 25, 2001, for MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.

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
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	AP	<u>19</u>	Plaintiff MIPS Technologies' Reply Brief In Support Of Its Position Regarding Claim Construction, 19 Pages, Filed November 6, 2000 in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AQ	<u>19</u>	MIPS Technologies' Claim Charts Pursuant To Local Rule 16-9(A), 14 Pages, Dated April 17, 2000, Filed in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AR	<u>19</u>	Order On Claim Construction, 2 Pages, Dated September 11, 2001, Filed in MIPS Technologies, Inc. v. Lexra, Inc., United States District Court for the Northern District of California- Oakland Division, Case No. C99-04766 SBA.
	AS	<u>19</u>	

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